Serial No.: DIVN. of USSN 09/320,543   Attorney's Docket No.:FUR0005-DIV.2
Art Unit: Not yet Assigned   Page 7
Inventor: Takuya SEKIGUCHI et al.

REMARKS

This application is a division of U.S. Patent Application Serial No. 09/320,543, filed May

27, 1999.

Claims 1-40 were originally filed. In this Preliminary Amendment, Applicant has

canceled claims 1-40 and added new claims 41-52. Accordingly, claims 41-52 will be pending

herein upon entry of this Preliminary Amendment, prior to calculation of the filing fee and

examination on the merits. No new matter has been added.

Applicant respectfully requests a favorable action on the merits of the pending claims.

Should the Examiner have any questions or determine that any further action is desirable to place

this application in condition for issue, the Examiner is encouraged to telephone Applicant's

undersigned representative at the number listed below.

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MDB/ggb

FIG.2

FIRST EMBODIMENT(GENERAL CONFIGURATION)
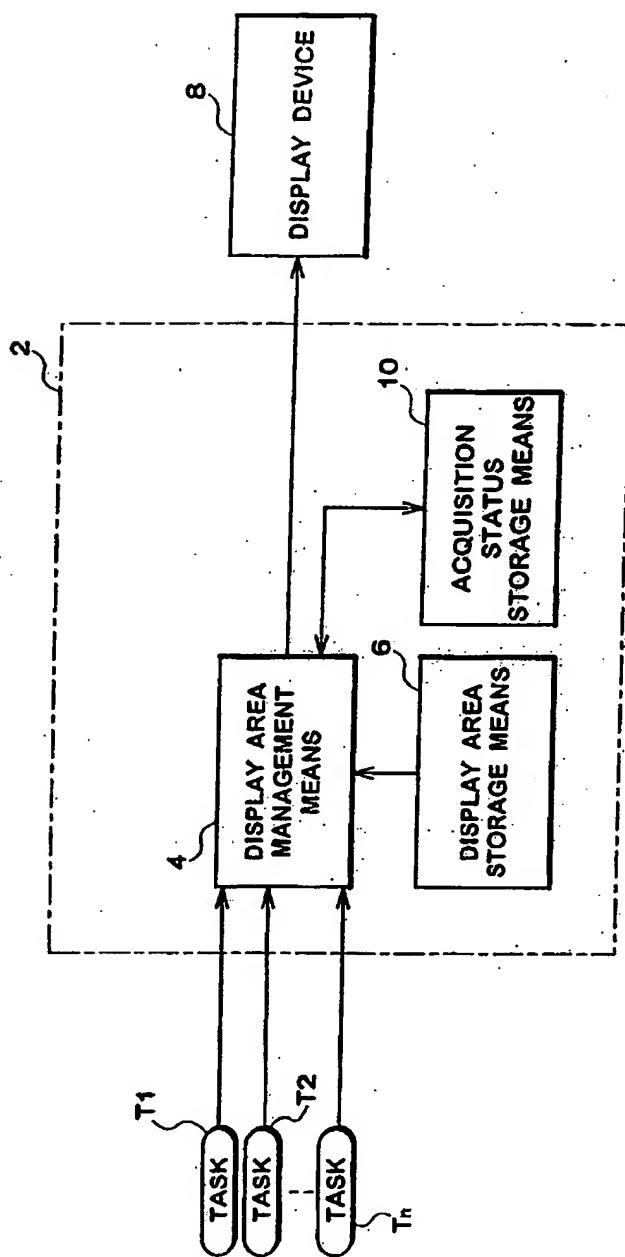
FIG.3

FIRST EMBODIMENT (HARDWARE CONFIGURATION)

FIG.4

34:AV DECODER

FROM CPU12

FROM TS DECODER 32

41 DECOMPRESSION CIRCUIT

42 V-RAM

44 COMPOSITE SIGNAL GENERATION CIRCUIT

TO TV SET36
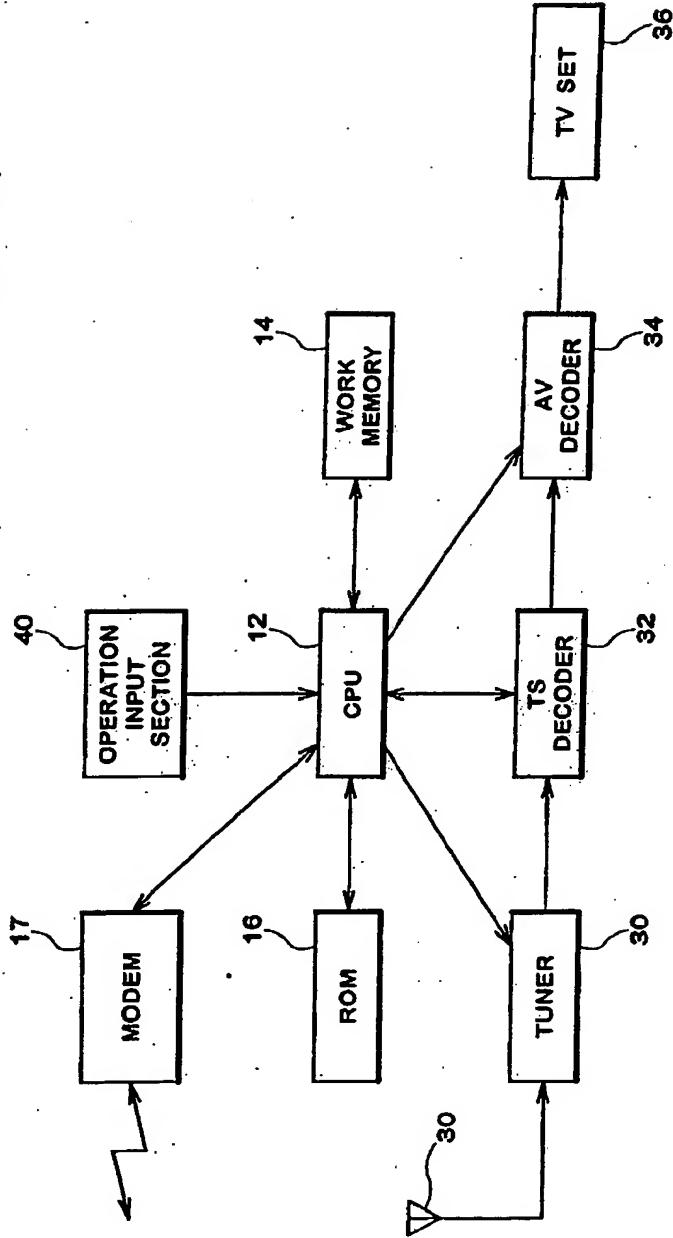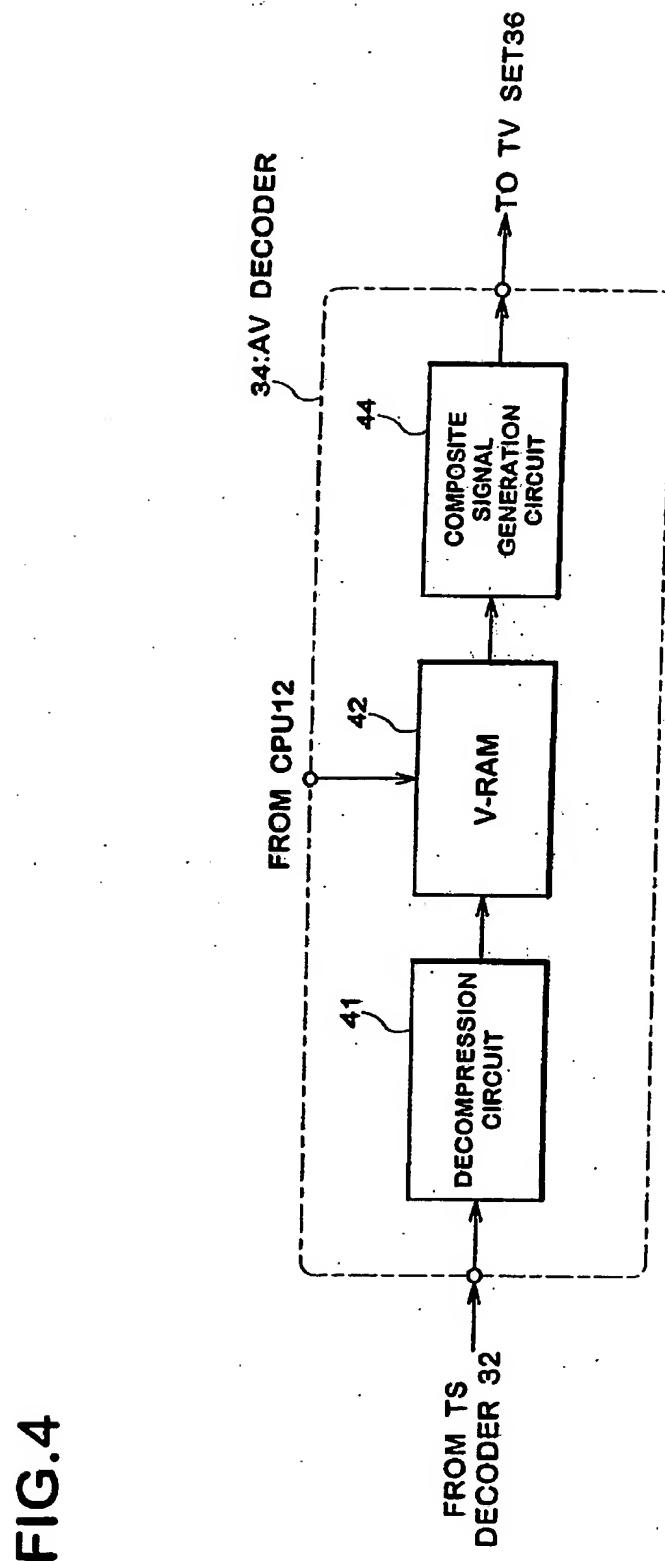
FIG.6

DISPLAY AREA DEFINITION TABLE

AREA	ORIGIN COORDINATE	WIDTH	HEIGHT	REMARKS
E1	(0, 0)	340	210	UPPER LEFT HALF SCREEN AREA
E2	(340, 0)	340	210	UPPER RIGHT HALF SCREEN AREA
E3	(0, 210)	680	210	LOWER HALF SCREEN AREA

FIG.13

SECOND EMBODIMENT(GENERAL CONFIGURATION)

DISPLAY DEVICE

8

2

COEXISXTENCE RELATIONSHIP STORAGE MEANS

20

ACQUISITION STATUS STORAGE MEANS

10

DISPLAY AREA MANAGEMENT MEANS

4

DISPLAY AREA STORAGE MEANS

6

DISPLAY PROCESSING SUPERVISORY MEANS

22

TASK

TASK

TASK

TASK

TASK

R_1

R_2

T_{1m}

T_{2m}

T_{12}

T_{11}

T_{21}

R_n
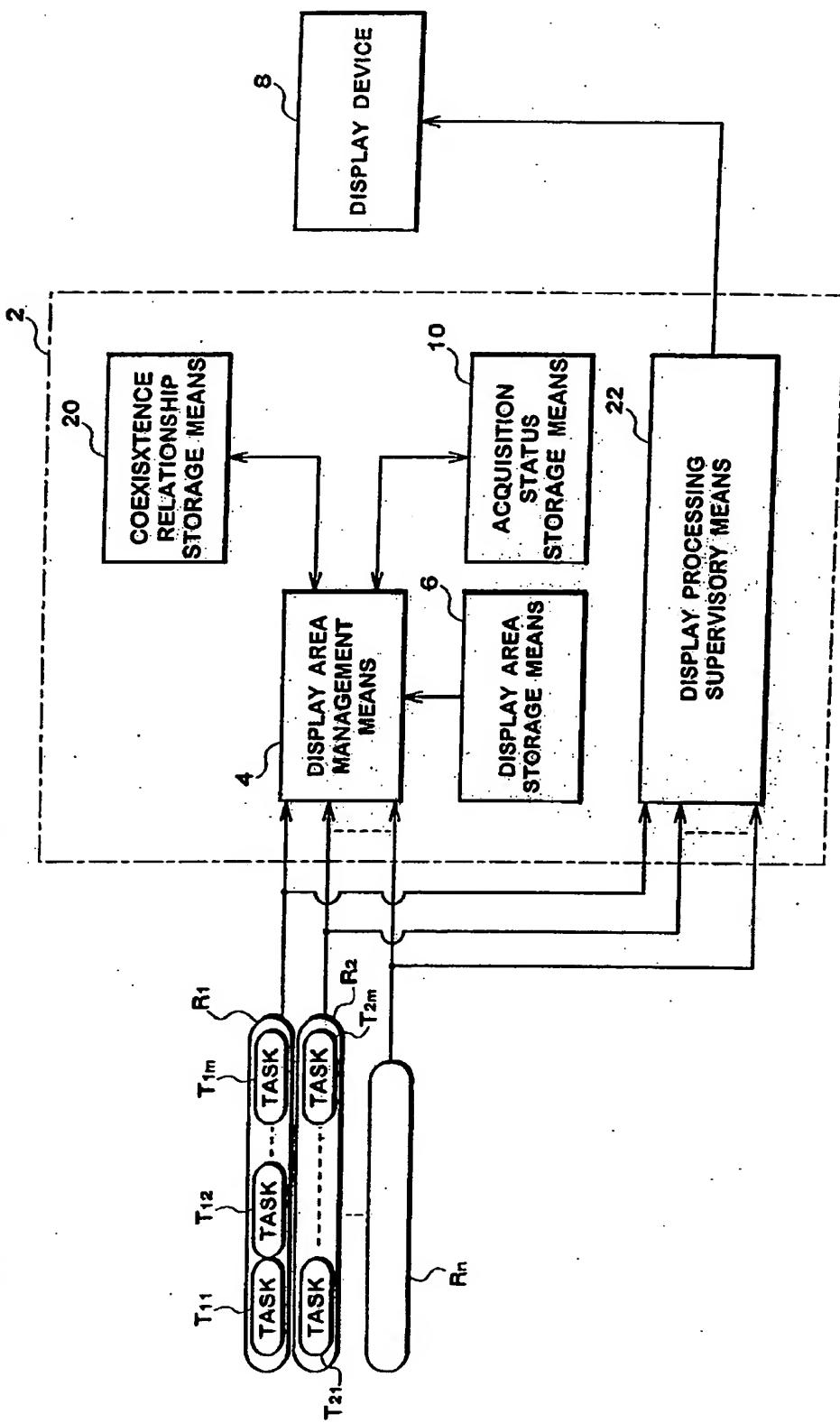
FIG.15

DISPLAY AREA DEFINITION TABLE

AREA	ORIGIN COORDINATE	WIDTH	HEIGHT	REMARKS
E1	(0, 0)	680	420	ENTIRE SCREEN AREA
E2	(0, 0)	340	420	LEFT HALF SCREEN AREA
E3	(340, 0)	340	420	RIGHT HALF SCREEN AREA
E4	(0, 0)	680	210	UPPER HALF SCREEN AREA
E5	(0, 210)	680	210	LOWER HALF SCREEN AREA

FIG.32

FIFTH EMBODIMENT(GENERAL CONFIGURATION)

DISPLAY DEVICE — 8

2

DEPENDENCY RELATIONSHIP STORAGE MEANS — 24

DISPLAY TASK STORAGE MEANS — 10

DISPLAY AREA MANAGEMENT MEANS — 4

DISPLAY AREA STORAGE MEANS — 6

DISPLAY PROCESSING SUPERVISORY MEANS — 22

PROCESSING UNIT — R1
PROCESSING UNIT — R2
PROCESSING UNIT — Rn
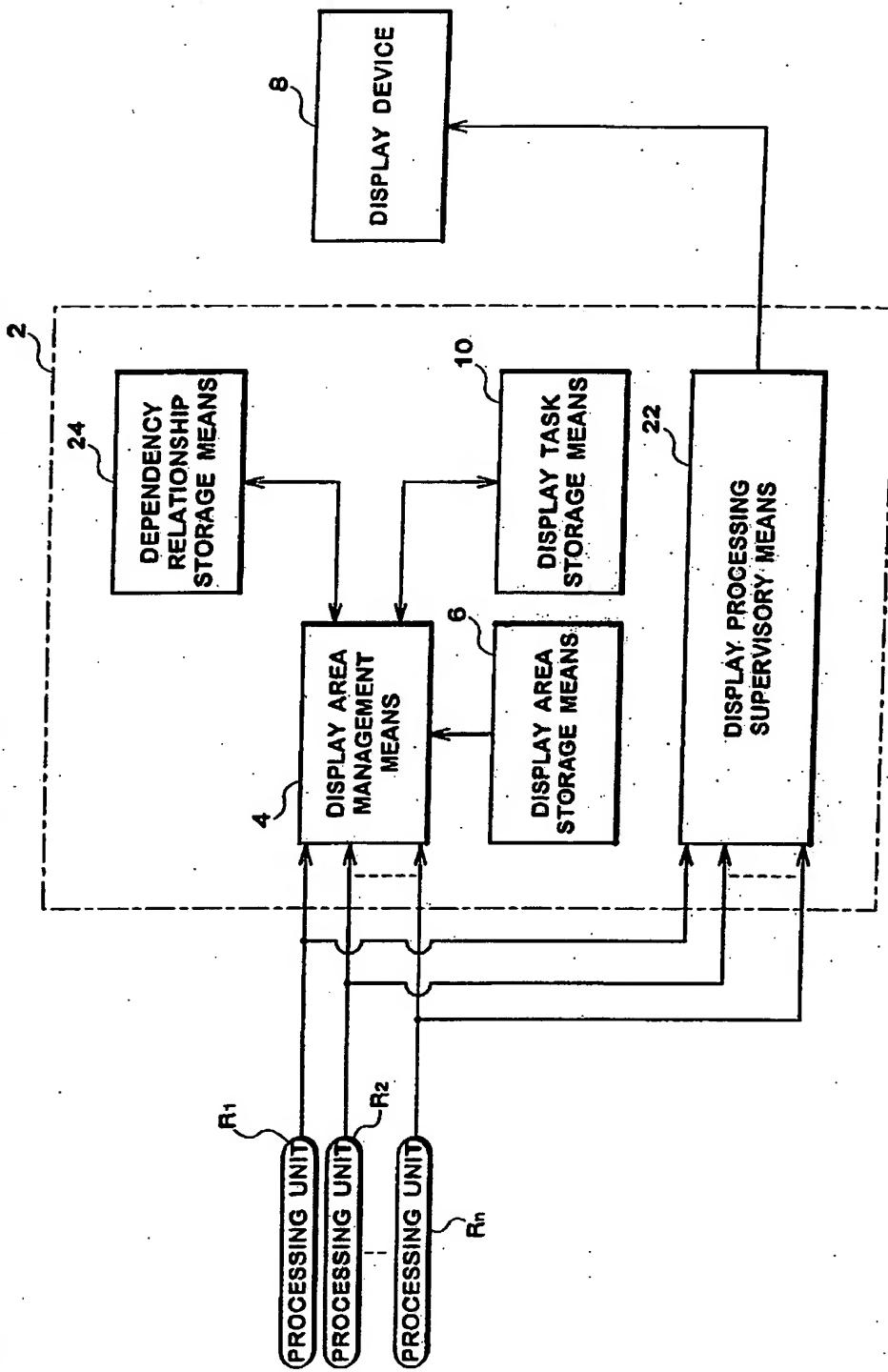
FIG.33

DEPENDENCY RELATIONSHIP TABLE

AREA	AREA TO DEPEND ON	AREA TO BE CHANGED
!	!	!
E2	E1	E3
E2	E4	E3
E2	E5	E3
!	!	!
E5	E1	E4

FIG.36

SIXTH EMBODIMENT(GENERAL CONFIGURATION)
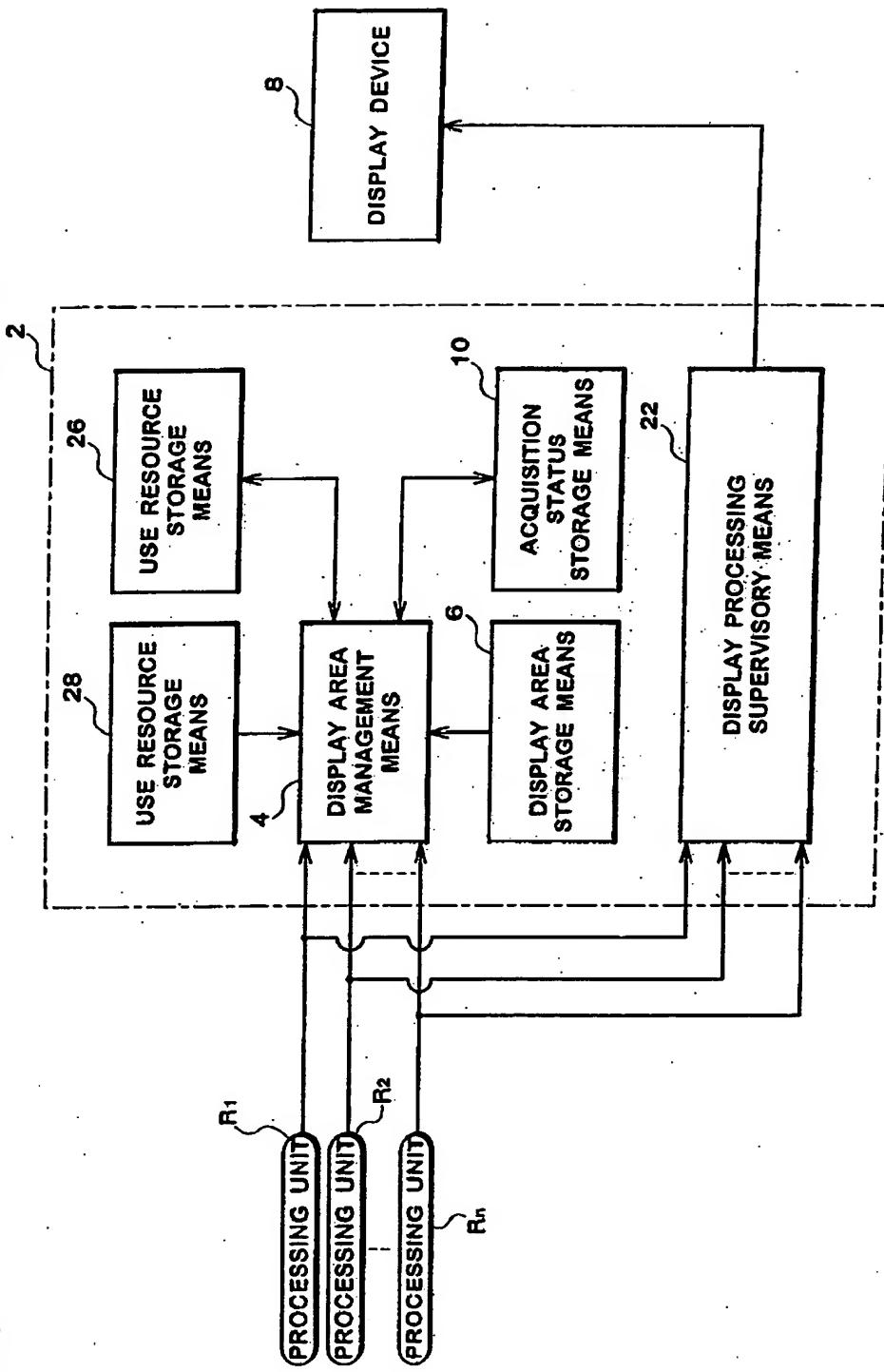
FIG.39

SIXTH EMBODIMENT
(FLOW CHART OF ACQUISITION REQUEST PROCESSING)

S701 — DISPLAY AREA ACQUISITION REQUEST

S702 — CHECKS USE STATUS OF DISPLAY AREA

S703 — ALREADY ACQUIRED BY ANOTHER PROCESSING UNIT ?

— YES →

NO ↓

S704 — EXTRACTS AVAILABLE RESOURCES

S705 — CHECKS USE STATUS OF VAILABLE RESOURCES

S706 — ALREADY USED BY ANOTHER PROCESSING UNIT ?

— YES →

NO ↓

S710 — RETURNS ACQUISITION FAILURE TO PROCESSING UNIT

S707 — STORES NECESSARY INFORMATION TO ACQUISITION STATUS STORAGE SECTION

S708 — RETURNS KEY NUMBER TO PROCESSING UNIT

S709 — PROCESSING END

FIG.47

DISPLAY AREA DEFINITION TABLE

AREA	ORIGIN COORDINATE	WIDTH	HEIGHT	REMARKS
E1	(0, 0)	340	210	UPPER LEFT HALF SCREEN AREA
E2	(340, 0)	340	210	UPPER RIGHT HALF SCREEN AREA
E3	(0, 210)	680	210	LOWER HALF SCREEN AREA

FIG.48

ACQUISITION RIGHT INFORMATION TABLE

AREA	TASK
E1	T1
E2	T2
E3	T3

FIG.49

EIGHTH EMBODIMENT
(FLOW CHART OF ACQUISITION REQUEST PROCESSING)